

## ABSTRACT

The present invention relates to an off-chip driver circuit and a data output circuit using the same. In the off-chip driver circuit having off-chip drivers each for using a data signal and control signals to determine whether to  
5 produce output signals according to corresponding control signals, at least one of the off-chip drivers includes a delay circuit(s) for outputting a signal having a given delay time compared with other signals of the remaining off-chip drivers. Therefore, since an output waveform can have an adequate slew, it is possible to enhance the properties of a system interface.